



PATENT  
450100-04889

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicants: Hideo NAKAYA et al.  
Serial No.: 10/762,996  
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For: INTEGRATED CIRCUIT AND INFORMATION  
SIGNAL PROCESSING APPARATUS  
Examiner: Kostak, Victor R  
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Confirmation No.: 6199

745 Fifth Avenue  
New York, NY 10151

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Mail Stop Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on October 17, 2007.

Thomas F. Presson, Reg. No. 41,442

Name of Applicant, Assignee or Registered Representative

Signature

October 17, 2007

Date of Signature

**INFORMATION DISCLOSURE STATEMENT**  
**UNDER 37 C.F.R. § 1.97 (c)**  
**AND CERTIFICATION UNDER 37 C.F.R. § 1.97 (e)**

Mail Stop Amendment  
Commissioner for Patents  
P.O. Box 1450  
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Sir:

In compliance with the duty of disclosure set forth in 37 C.F.R. 1.56, applicants are filing this Information Disclosure Statement and the accompanying form PTO-1449 along

with copies of the prior art therein identified. Such prior art has come to the attention of applicants by being cited in an Office Action issued in the corresponding Japanese application on September 18, 2007. So far as applicants are aware, this Information Disclosure Statement is being filed before the mailing date of the final Office Action or Notice of Allowance.

Applicants' undersigned attorney hereby certifies pursuant to 37 C.F.R. 1.97(e) that each item of information contained in this Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this statement.

The filing of this Information Disclosure Statement is not an admission that the documents identified herein constitute prior art to the present application.

Please charge any fees incurred by the filing of this document or credit any overpayment to Deposit Account No. 50-0320.

Respectfully submitted,

FROMMER LAWRENCE & HAUG LLP  
Attorneys for Applicants

By: 

Thomas F. Presson  
Reg. No. 41,442  
Tel. (212) 588-0800